



VCOR6171SMUA4R5_DBL_135

rev_0	initial rev
rev_1	pkg clarify
rev_2	pkg update

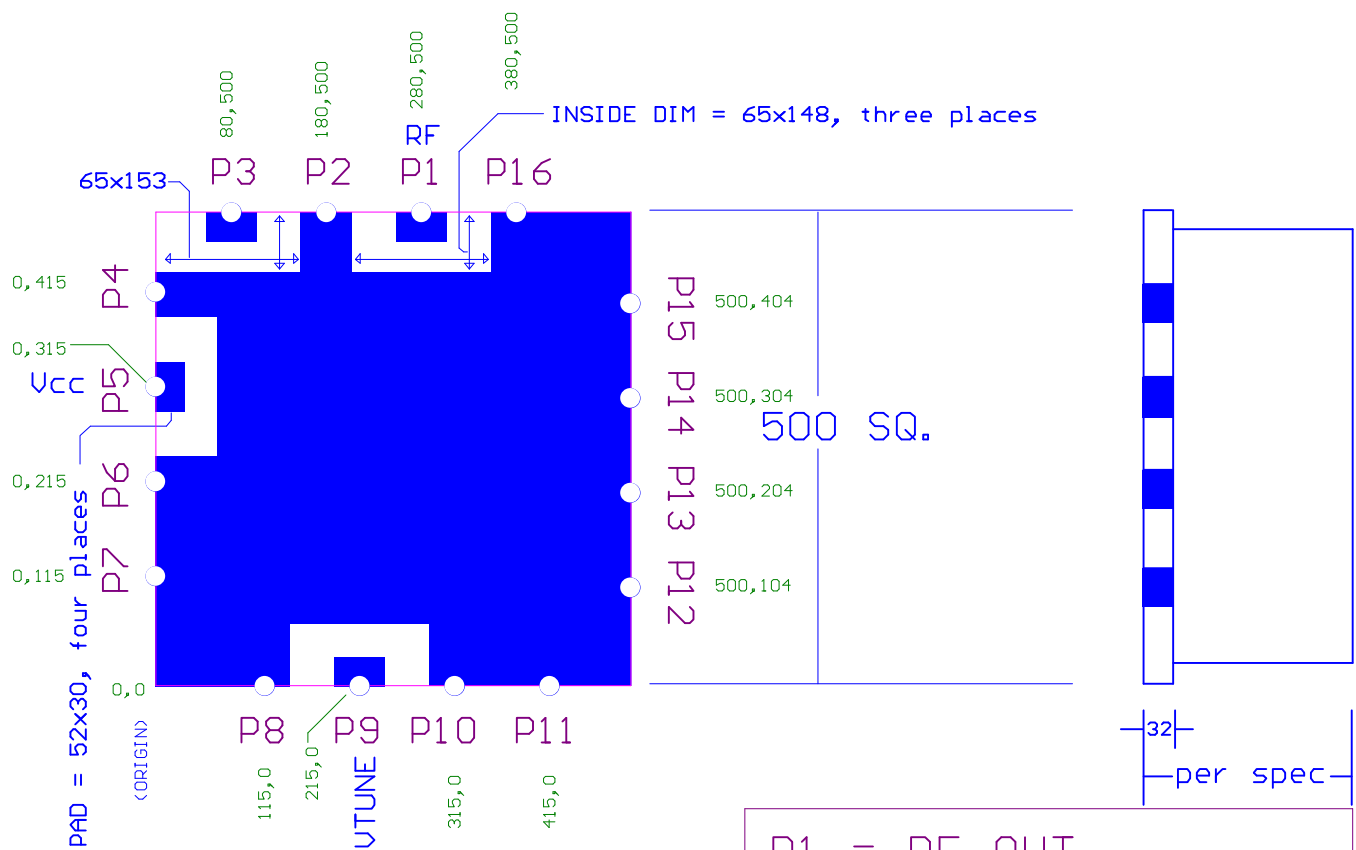
	<u>MIN</u>	<u>TYP</u>	<u>MAX</u>	<u>UNITS</u>	<u>NOTES</u>	<u>Production</u> <u>Test</u>	<u>First Article</u> <u>Test</u>
Frequency Range	6100		7100	MHz	1	..	..
Tuning Voltage	1		16	V	1	..	..
Modulation Sensitivity		75		MHz/V	1	..	..
Operates with Vtune = 0V					1	..	..
Output Power	-4	-1	2	dBm	1	..	..
Phase Noise @							
1 KHz Offset		-63	-58	dBc/Hz	1	..	..
10 KHz Offset		-90	-85	dBc/Hz	1	..	..
100 KHz Offset		-110	-105	dBc/Hz	1	..	..
1 MHz Offset		-130	-125	dBc/Hz	1	..	..
10 MHz Offset		-150	-145	dBc/Hz	1	..	..
Pushing		7	14	MHz/V	3	..	..
Pulling (12dB RL, any phase)		0.2	1	MHz _{p-p}	3	..	..
Modulation Bandwidth		1		MHz	4	..	..
Tuning Port Capacitance		20		pF	2	..	..
Harmonics		-18	-10	dBc	3	..	..
Sub-Harmonics		-20	-10	dBc	3	..	..
Vcc		4.5		V			
Icc		50		mA	1	..	..
Operating Temperature	-40		+85	C	3	..	..
Package		SMUA_DBL			6	..	..
LxW		0.500		inch sq.	5	..	..
Height		0.135		inch	5	..	..

Notes

1. Tested in production, and guaranteed
2. Per current design, not a tested parameter
3. Sample tested from production lot
4. Not a tested or guaranteed design goal
5. By inspection
6. RoHS Compliant

DBL FOOTPRINT

TOP VIEW, BOT SIDE



NOTES

1. Bottom is solid ground plane.
2. All dimensions in mils
3. All holes are plated through.

P1 = RF OUT
 P3 = 1/2 FREQUENCY
 P5 = VCC
 P9 = VTUNE
 ALL OTHER PINS GND